



$$I_{D0} = 10 \text{ mA}$$

NATIONAL Semiconductor FET NPD 5564

duct-in-line package low noise matched pair

DRAIN resistor must be selected for individual FET used.

(values shown for  $R_D$  and  $R_S$  approximate only)

### DENNISEN ANTARES PREAMP

pac-show version

features extreme low-noise front end  
 split RIAA equalization for  
 greatest flexibility in gain  
 adjustment, non-interaction  
 of time constants, and  
 manageable input for high-speed  
 low-noise op amp.

TR