

## 5. CIRCUIT DESCRIPTIONS

The SP-101 is a synthesized surround processor that produces four effects based on the surround data included in surround encoded Laser discs and ordinary stereo broadcasts to give the listener the impression of being in different listening environments. It also has a super bass synthesizer section to allow the listener to really "feel" the music.

### 5.1 SUPER BASS SYNTHESIZER CIRCUIT

This circuit increases the feeling of volume by synthesizing lower harmonics of low range signals to add extra power and depth to low frequency sounds. These lower harmonics are produced by dividing the frequency of the input signal in half.

The input signal is first routed through a 150Hz lowpass filter and then four band-pass filters with poles at 63Hz, 80Hz, 100Hz and 125Hz respectively. The output of each band-pass filter is split into two routes, one to a level detector circuit and the other to a limiter circuit. The level detector circuit is an absolute value detector circuit using an operation amp that converts the signal level into a DC voltage. The limiter circuit changes the signal into a square wave by causing the operation amp to operate at saturation.

The frequency of the limiter circuit output signal is divided in half by the T flip-flop and sent to VCA (an amp in which gain can be changed with a control voltage). The gain of each VCA is controlled by the output voltage of the corresponding level detector circuit so that the signal level (of the square wave obtained after dividing the input signal frequency in half) is always proportional to the level of the original input signal. All four VCA output signals are mixed and the upper harmonics of the square wave are removed by a low-pass filter so that the signal contains only lower harmonics. After the level of this signal is adjusted by the SYNTHE. LEVEL control (a double volume), it is routed through a low-pass filter amp before being mixed with the FRONT L/R and SURROUND L/R signals.

Input to the 63Hz and 80Hz circuits can be shut out with the front panel 30Hz and 40Hz CUT switches (the front panel frequency indication rounds off the halved frequencies to the nearest ten). These two switches are provided to allow the user to reduce the super bass effect when desired to avoid the unpleasant effect that can result when too much bass is added to some kinds of music.

The output of each level detector circuit causes each front panel frequency indicator LED to light.

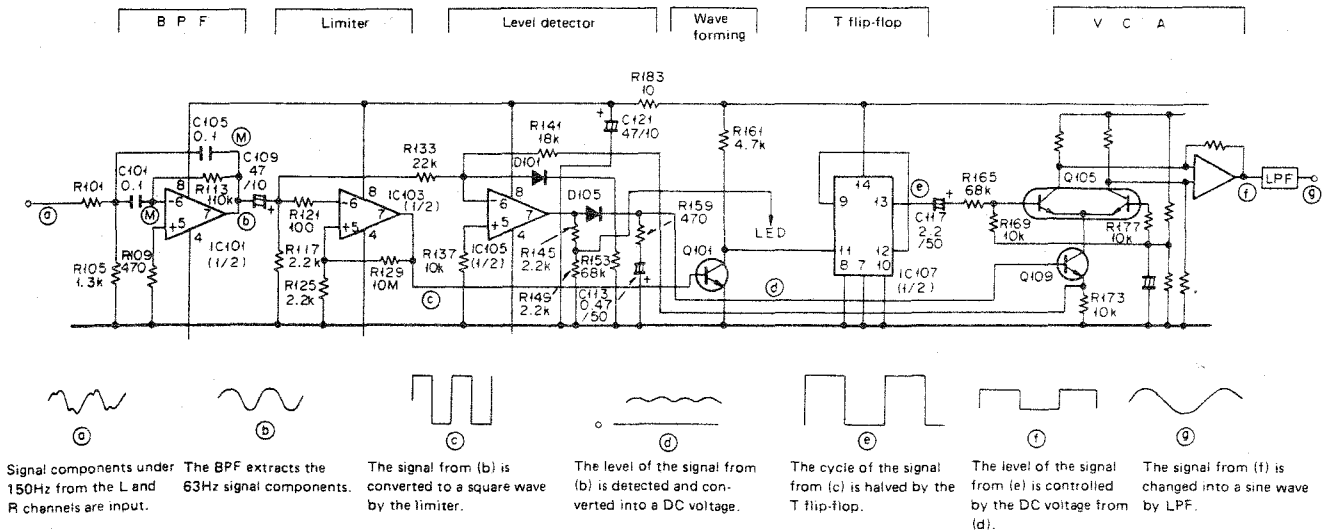


Fig. 5-1 The Super Bass Synthesizer Circuit

## 5.2 SURROUND PROCESSOR CIRCUIT (Fig. 5-3)

This circuit synthesizes signals for two more channels by processing the original L and R channel signals to produce the desired feeling of ambience. The signals of the two additional channels are created by making different combinations of the positive and reverse phases of the L and R channel input signals and the two delay ICs, each having a different delay time. This allows the circuit to produce four different effects.

The delay circuit is composed of a 4096-stage BBD IC (MN3005) and a 2048-stage BBD IC (MN3008). Both ICs are driven by a 33 kHz 2-phase clock signal. To minimize the degradation of the S/N ratio caused by the use of BBDs, compander ICs (NE571) are used to compress the dynamic range of the signal on the input side of the delay circuits. The dynamic range is then returned to its original width on the output side by an expander, thereby reducing noise.

### Simulated Stereo Mode

The SURROUND L output signal is the L channel signal that has passed through the 4096-stage BBD delay circuit. The SURROUND R output signal is obtained by reversing the phase of the original R channel signal and then passing that signal through the 2048-stage BBD delay circuit. When listening to bilingual and monaural sources, this mode gives sound a feeling that resembles stereo reproduction.

### Studio Mode

The SURROUND L and R output signals are used to produce L and R channel differential signals (L-R, mono).

### Theater Mode

The SURROUND L output signal is the L and R channel differential signal (L-R) that has passed through the 4096-stage BBD delay circuit. The SURROUND R output signal is the differential signal (L-R) that has passed through the 2048-stage BBD delay circuit.

### Stadium Mode

The SURROUND L output signal is the L and R channel differential signal (L-R) that has passed through the 4096-stage BBD delay circuit. The SURROUND R output signal is the differential signal (L-R) without any delay added. Both of these signals are output at a higher level than in the other three modes.

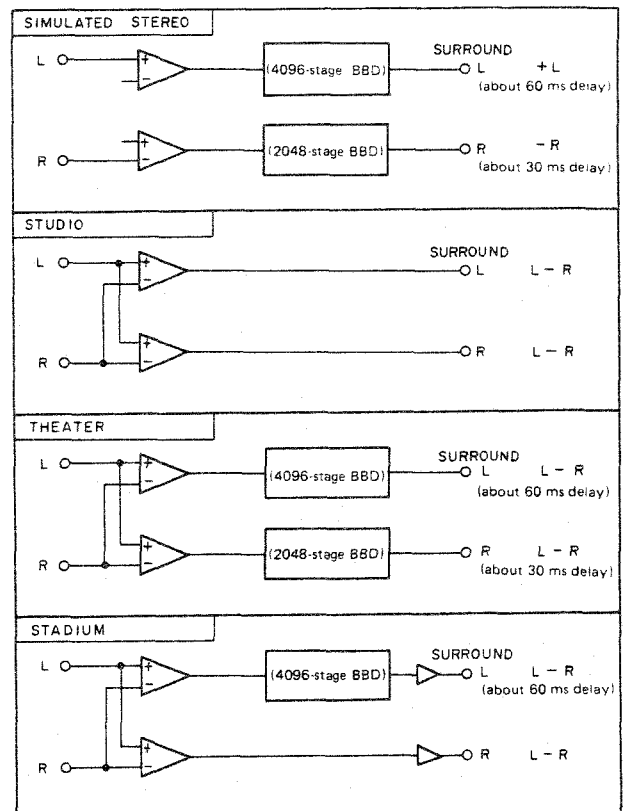


Fig. 5-2 Signal Processing in the Four Surround Decoder Modes

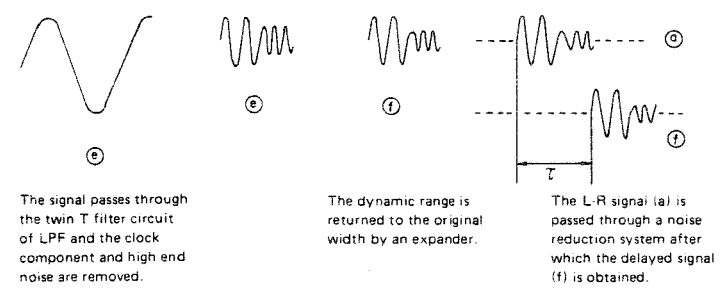
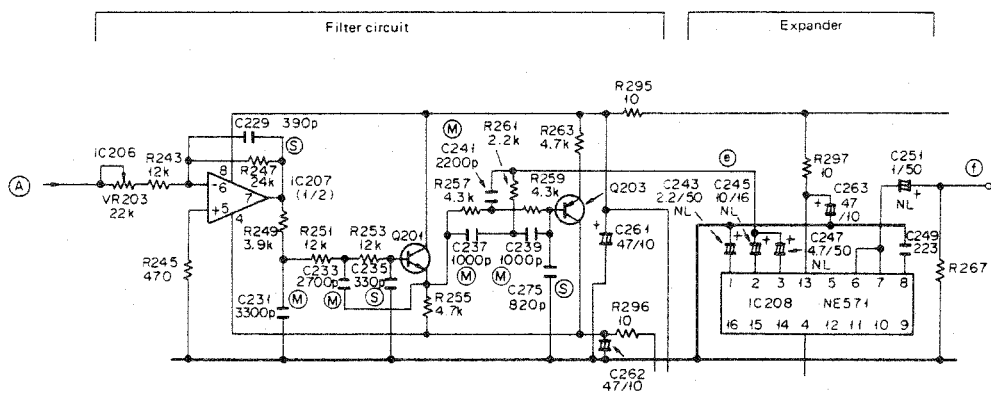
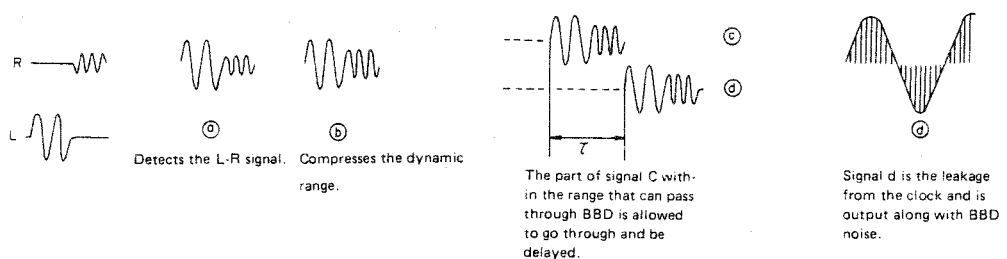
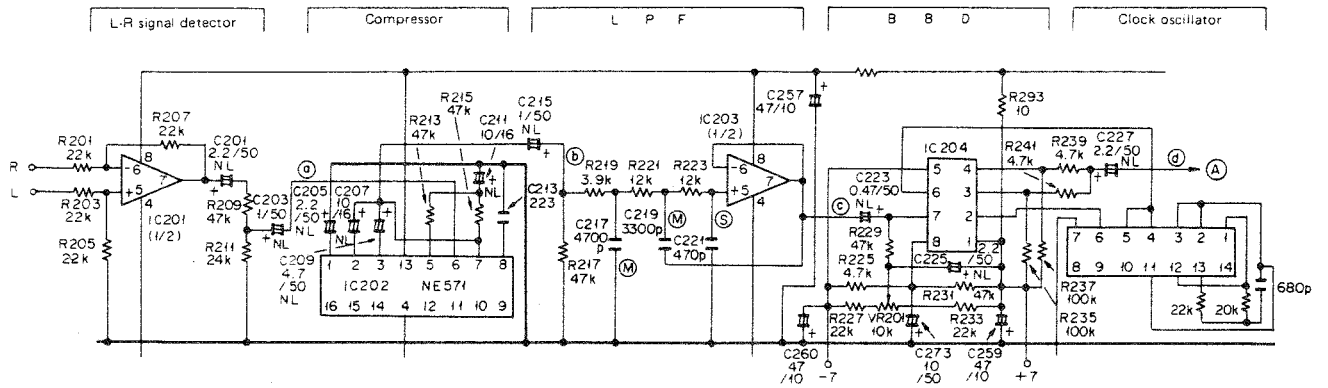
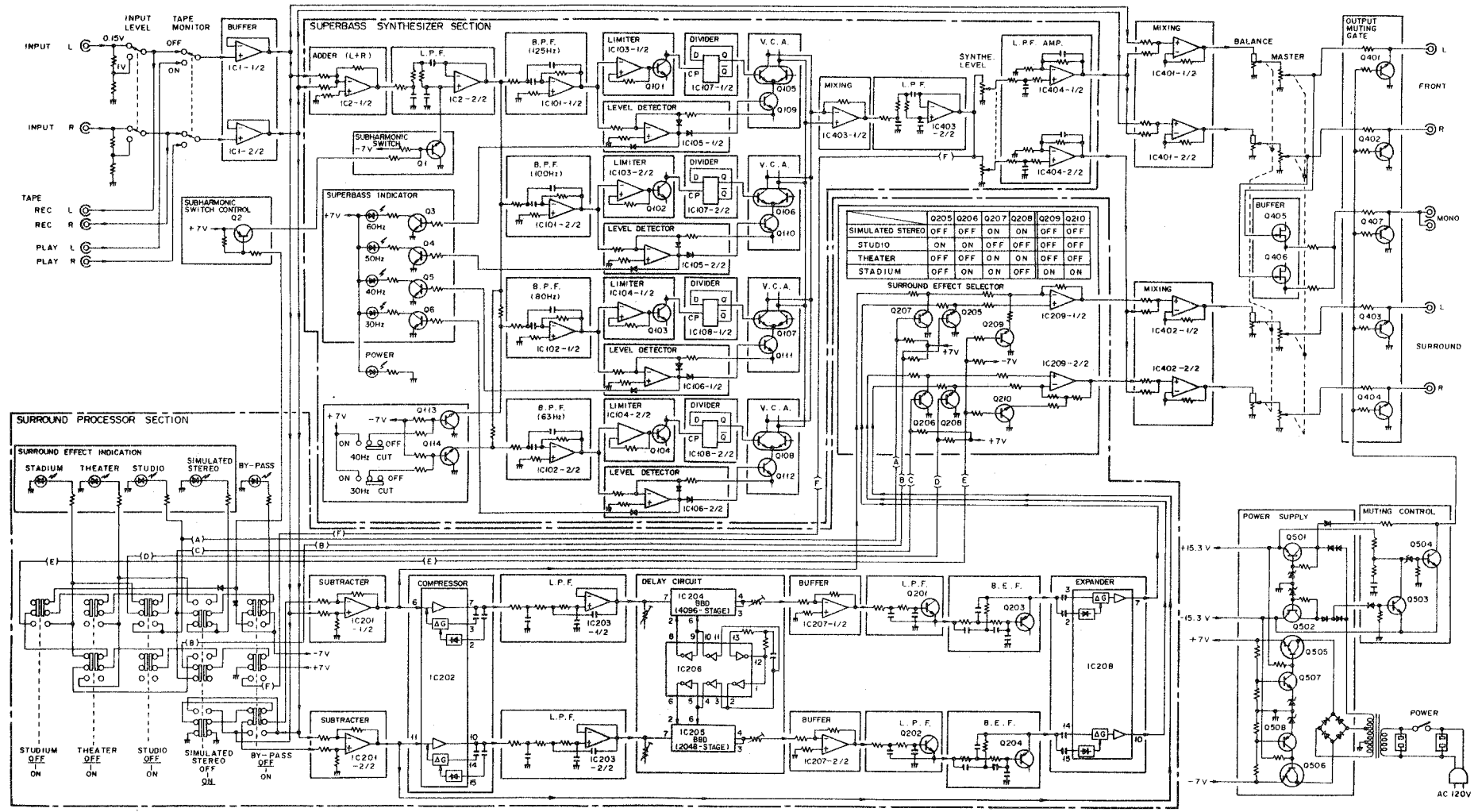


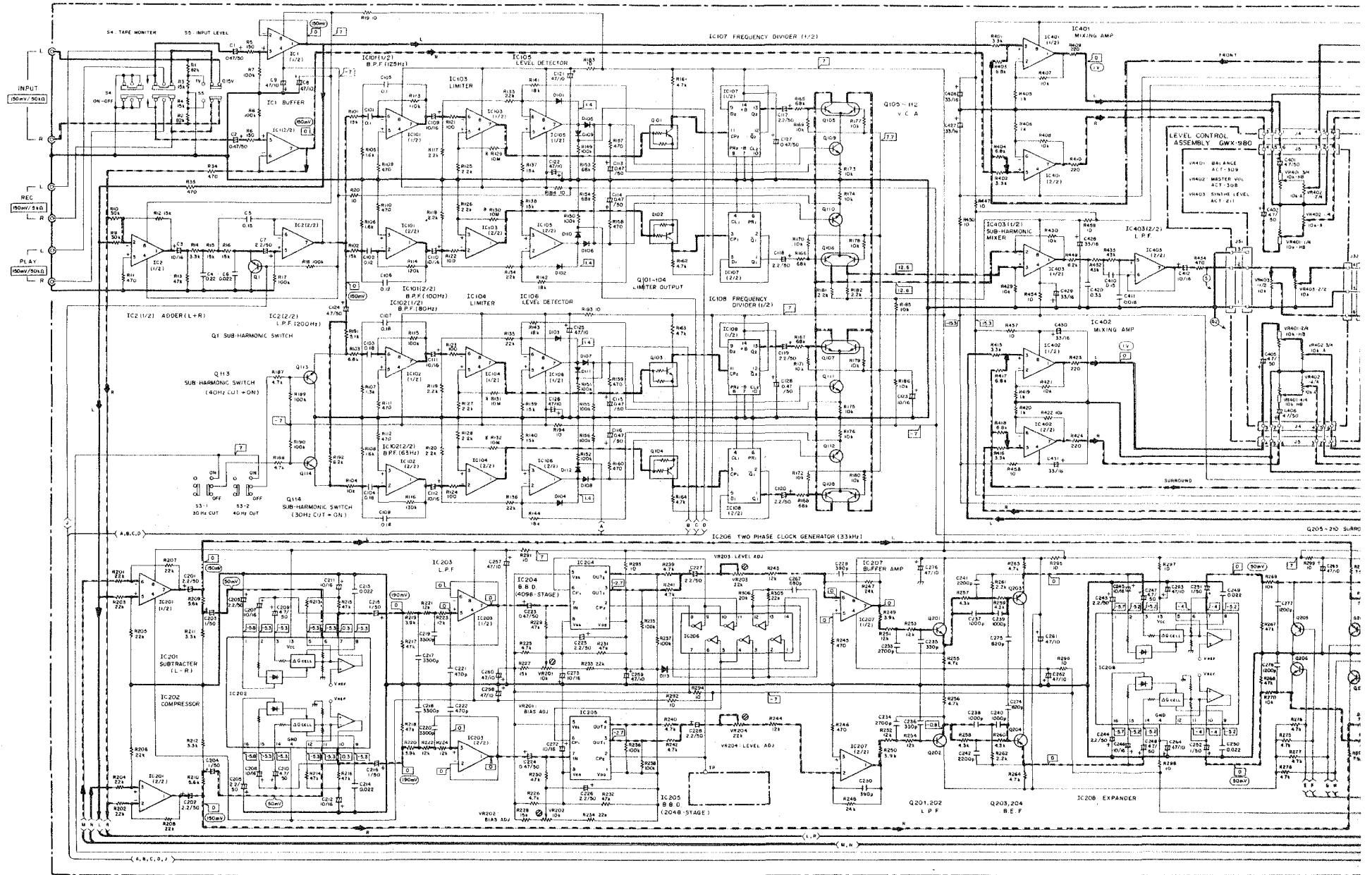
Fig. 5-3 The Sound Processor Circuit

# 6. BLOCK DIAGRAM



# 10. SCHEMATIC DIAGRAM

COMPLEX ASSEMBLY GWM-341



A

B

C

D

1

2

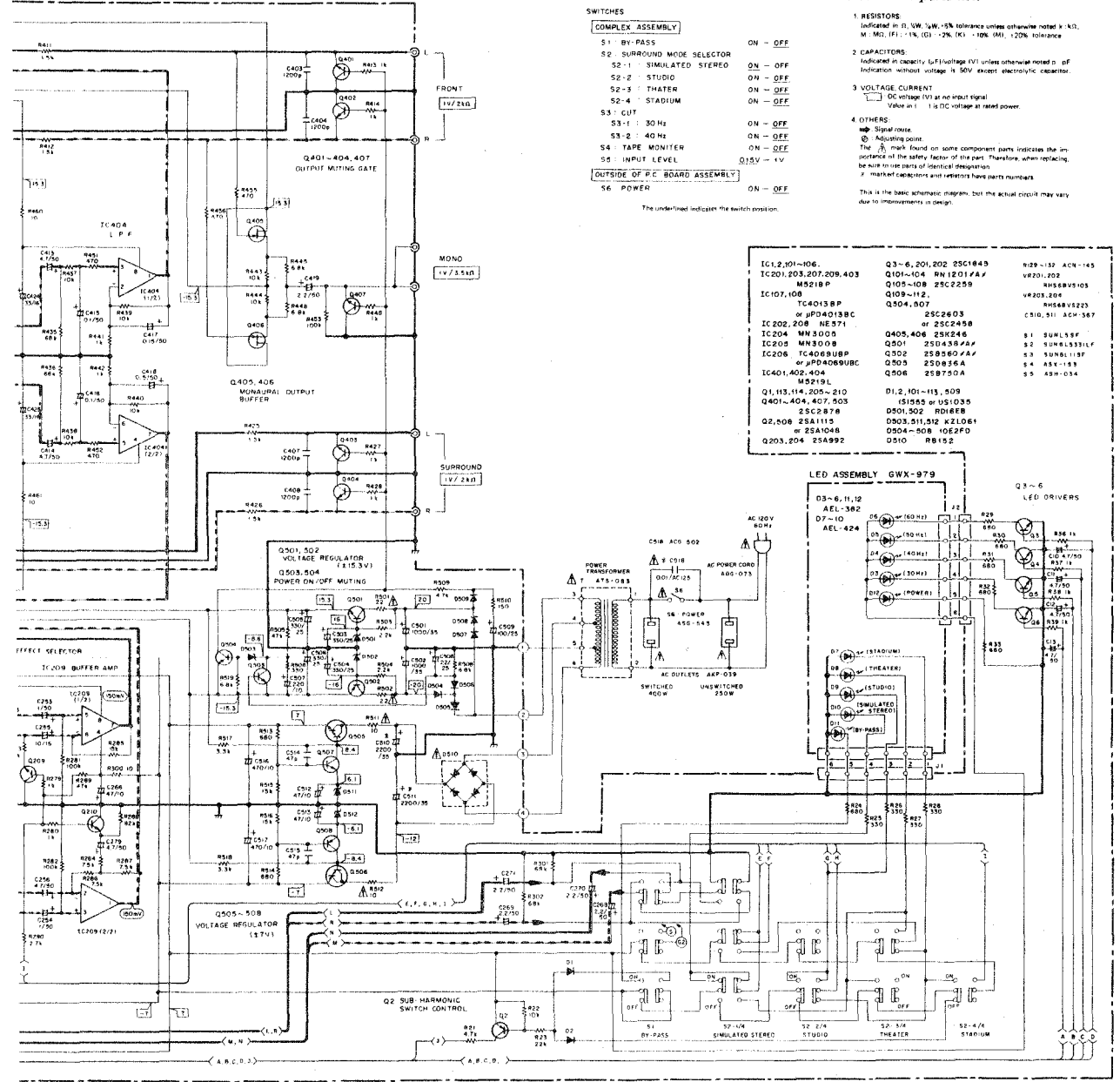
3

4

5

6

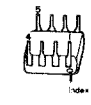
**NOTE:**  
The indicated semiconductors are representative ones only. Other alternative semiconductors may be used and are listed in the parts list.



- SWITCHES**
- COMPLEX ASSEMBLY**
- S1 BY-PASS ON - OFF
  - S2 SURROUND MODE SELECTOR ON - OFF
  - S2-1 SIMULATED STEREO ON - OFF
  - S2-2 STUDIO ON - OFF
  - S2-3 THEATER ON - OFF
  - S2-4 STADIUM ON - OFF
  - S3 CUT ON - OFF
  - S3-1 30 Hz ON - OFF
  - S3-2 40 Hz ON - OFF
  - S4 TAPE MONITOR ON - OFF
  - S5 INPUT LEVEL Q15V + V
- OUTSIDE OF P.C. BOARD ASSEMBLY**
- S6 POWER ON - OFF
- The underlined indicates the switch position.

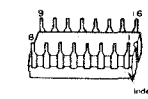
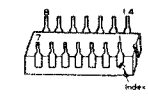
IC1,2,101-106	Q3-6, 201, 202 2SC1843	R129-132 ACN-143
IC201,203,207,209,403	Q101-104 RN1201/A/P	VR201,202
MS218 P	Q105-108 2SC2259	WR58W8V103
IC107,108	Q109-112, Q504,507	VR203,204
TC4013BP or JPD4013BC	2SC2603 or 2SC2458	WR58W1223
IC202,208 NE571	Q405,406 2SK246	CR10,111 RCN-387
IC204 MN3008	Q501 2SD438/A/P	S1 SURL15F
IC205 MN3008	Q501 2SD438/A/P	S2 SURL63311F
IC206 TC4069BUP or JPD4069UBC	Q502 2SD860 A/P	S3 SURL118F
IC401,402,404 MS219L	Q503 2SD836A	S4 ASA-153
Q1,113,114,205-210	Q506 2SB750A	S5 ASH-034
Q401-404, 407, 503 2SC2878	D1,2,101-113,508 (S1585 or US1035	
Q2,206 2SA1115 or 2SA1048	D501,502 RDB68	
Q203,204 2SA992	D903,911,912 KZL061	
	Q504-508 10E2FD	
	D510 RB152	

M5218P

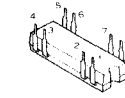


TC4013BP  
μ PD4013BC  
TC4069BUP  
μ PD4069UBC

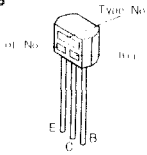
NE571



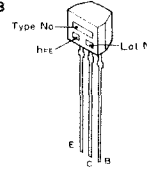
MN3005  
MN3008



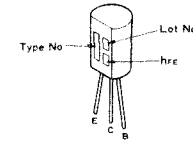
2SC2603  
2SA1115



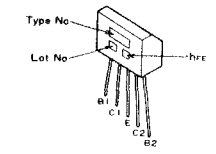
2SC2458  
2SA1048



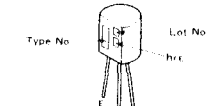
2SC1846  
2SA992



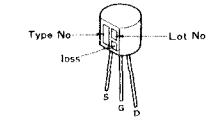
2SC2259



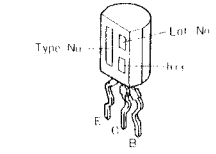
2SC2878



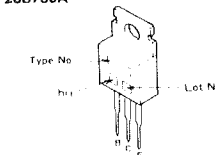
2SK246



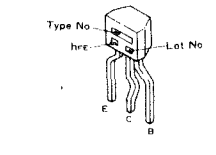
2SD438/A/  
2SB560/A/



2SD836A  
2SB750A



RN1201/A/



M5219L

